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EXAMINER: Michelle Estrada

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ART UNIT: 2823

SERIAL NO.: 09/784,424

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Applicant: Youri Ponomarev

Atty. Docket: NL 000139

Serial No.: 09/784,424

Art Unit: 2823

Filed: February 15, 2001

Examiner: Michelle Estrada

Title:

Semiconductor Device With an Integrated CMOS Circuit With

MOS Transistors Having Silicon-Germanium (Si_{1-x}Ge_x) Gate

Electrodes, and Method of Manufacturing Same

Assistant Commissioner for Patents Alexandria, VA 22313-1450

AMENDMENT INTRODUCTORY COMMENTS

In response to the Office Action dated August 13, 2003, Applicant submits the following amendments and remarks.

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